

ABSTRACT OF THE DISCLOSURE

In a semiconductor integrated circuit device, an n-channel transistor area has an area A on a pad side and an area B on an internal circuit side, where a plurality of protective elements are connected in parallel between a signal line and a power supply line. Each of the protective elements has resistors. Resistance of the resistors in the area A is set higher than resistance of the resistors in the area B by a value corresponding to resistance of parasitic resistance of the signal line included in the area A so that the resistance of the protective elements in the areas A and B are the same or almost the same as each other. A p-channel transistor area has the same configuration as that of the p-channel transistor area.